

ABSTRACT

A descrambler integrated circuit (IC) is adapted to receive scrambled digital content and to descramble the scrambled digital content. According to one embodiment of the invention, the descrambler comprises a first process block, a second process block and a descrambler. The first process block is configured to encrypt a message using a unique, one-time programmable key to produce a first key. The second process block is configured to receive an encrypted second key and, using the first key, to decrypt the encrypted second key in order to recover the second key in a non-encrypted format. The descrambler is configured to descramble the scrambled digital content using the second key in the non-encrypted format and to produce digital content in a clear format.